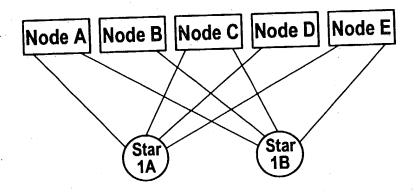
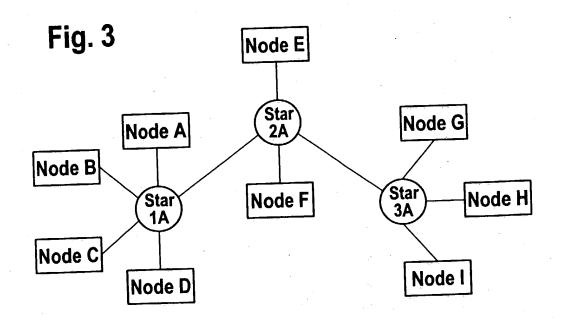


Fig. 2





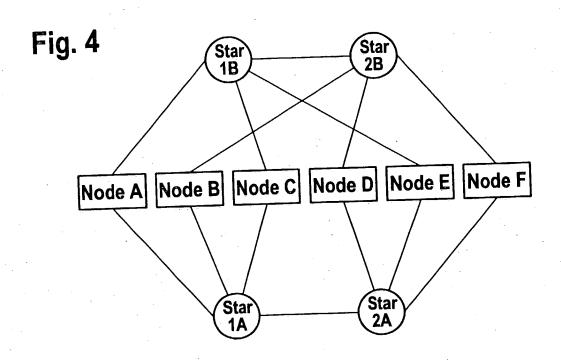


Fig. 5

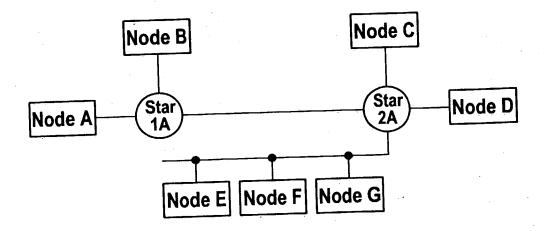


Fig. 6

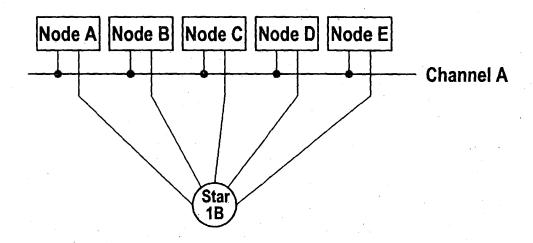


Fig. 7

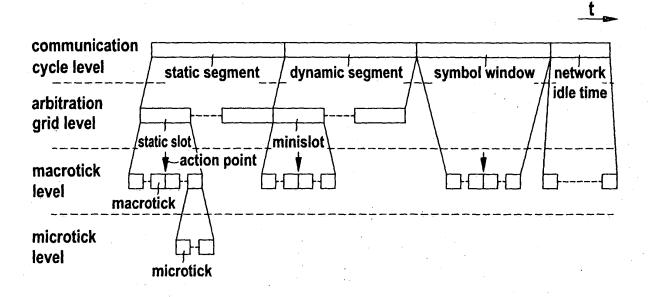


Fig. 8

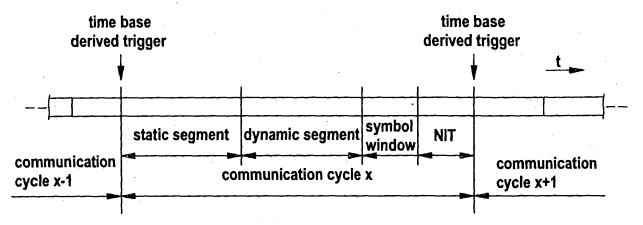


Fig. 9

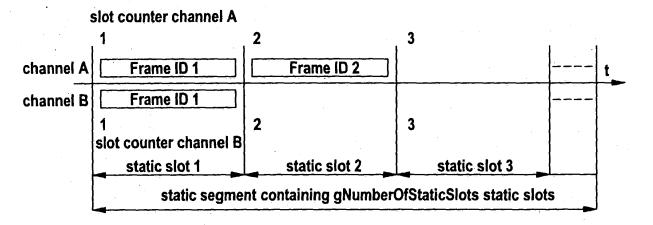


Fig. 10

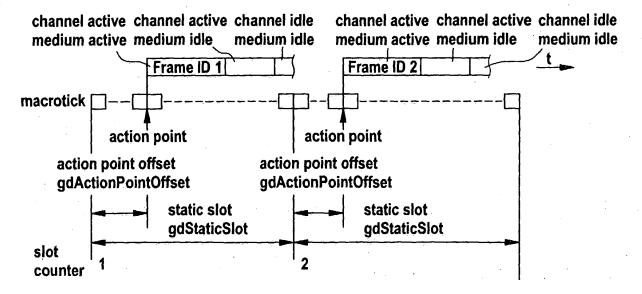
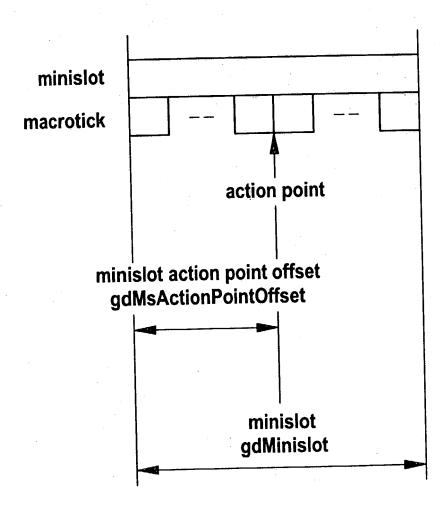


Fig. 12



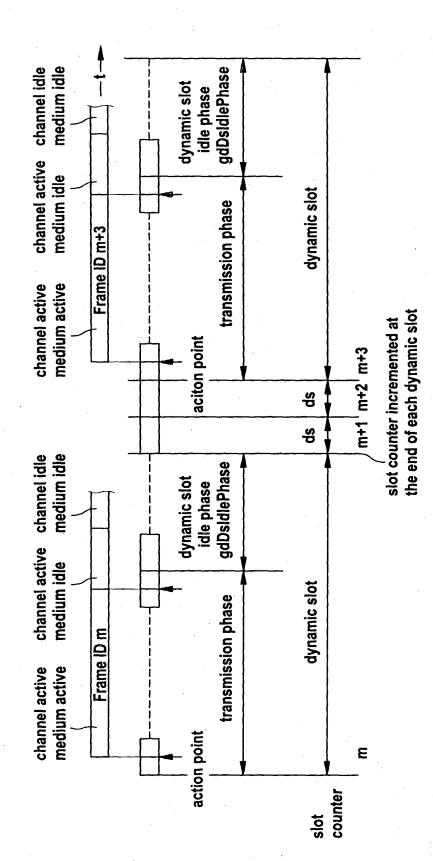


Fig. 13

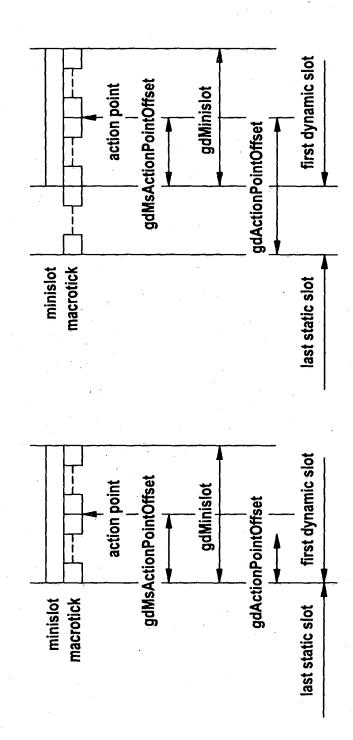


Fig. 14

Fig. 15

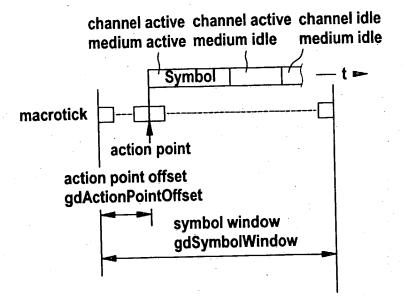
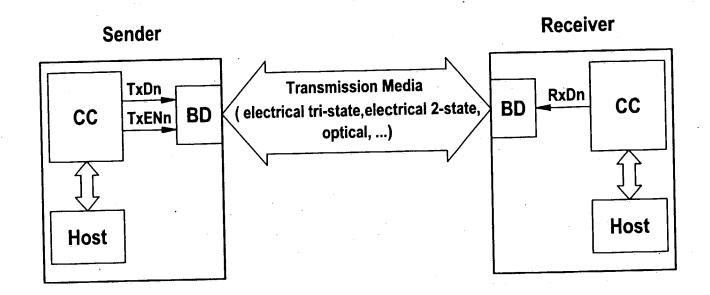
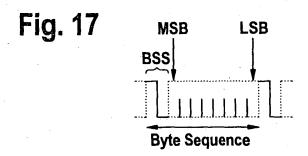
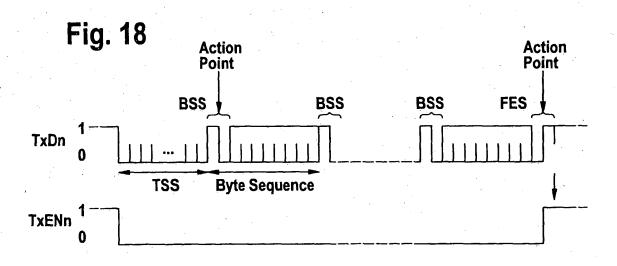
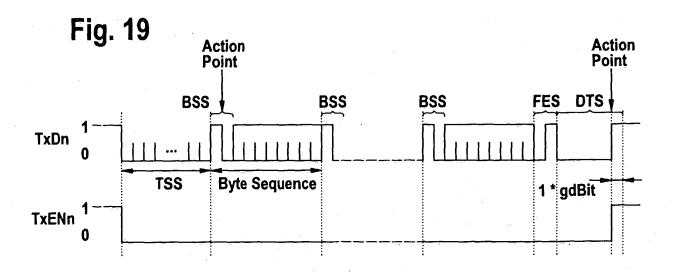


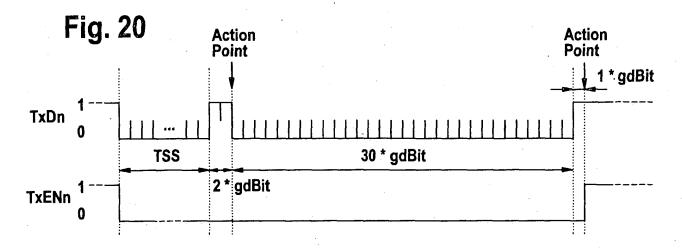
Fig. 16

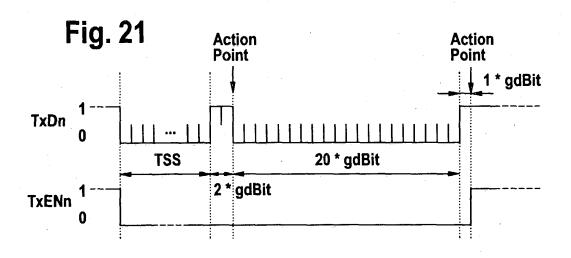












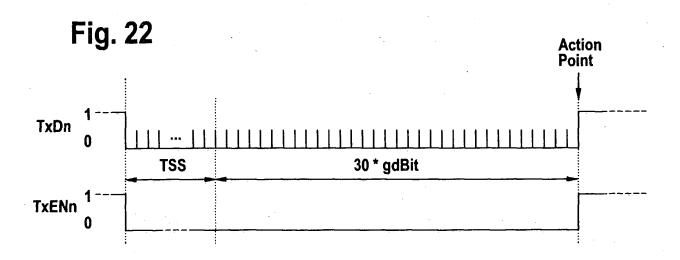


Fig. 23

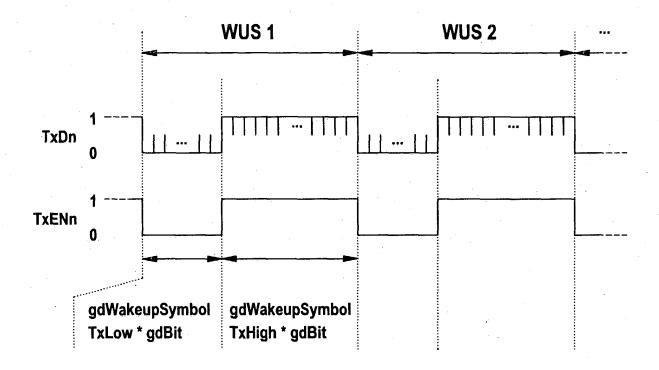


Fig. 24

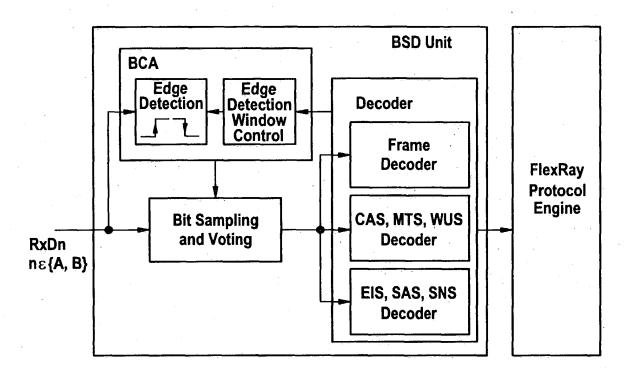


Fig. 25

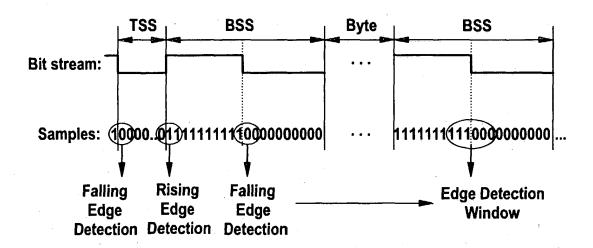


Fig. 26

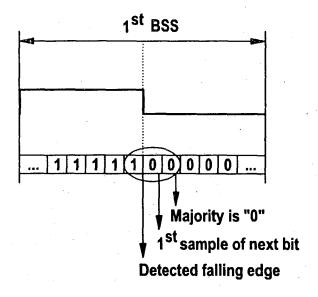


Fig. 27

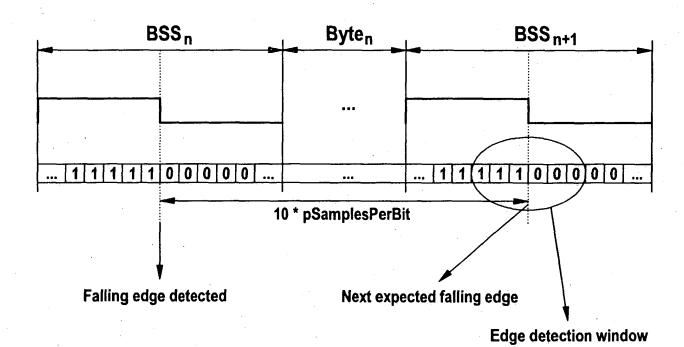
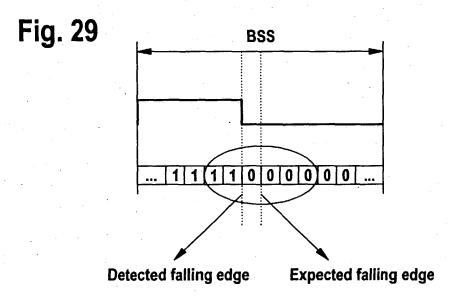


Fig. 28

BSS

... 1 1 1 1 1 1 0 0 0 0 0 ...

Expected falling edge Detected falling edge



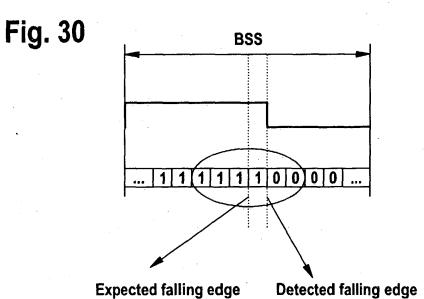
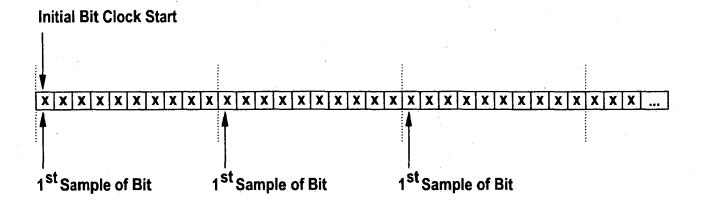


Fig. 31



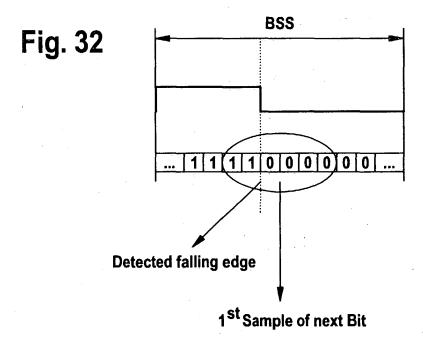


Fig. 33

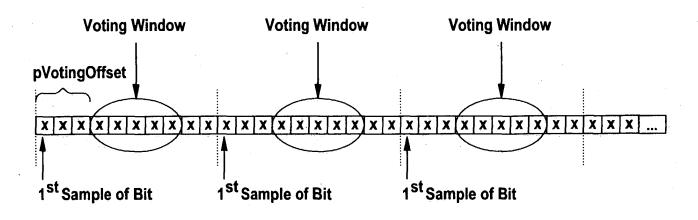


Fig. 34

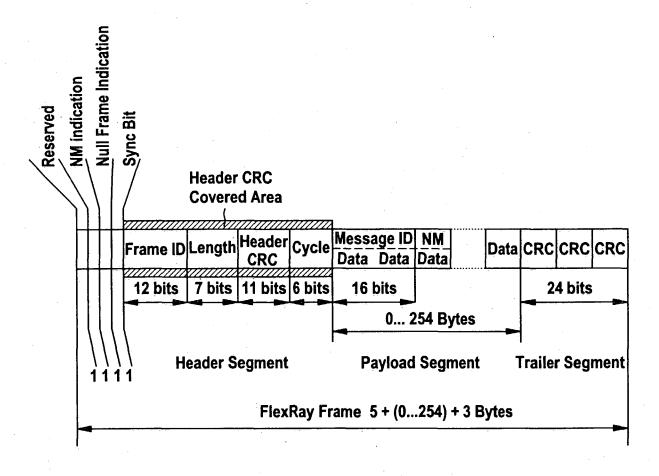


Fig. 35

ID	Res	Len	Data	Data	Data		Data	CRC	CRC	
8 bits	4	4	012 Bytes					15 bits		1 bit (FCB)
i I Header Seg.				Payload Segment				Trailer Seg.		1
		b	yteflig	ht Fran	ne 2+ (012)	+ 2 By	tes		
•				 	1 1987 9	· · · ·				*

Fig. 36

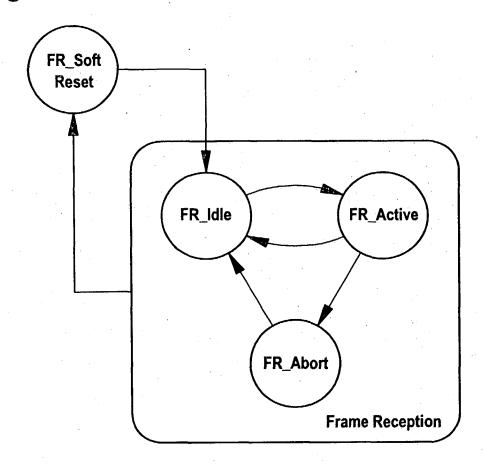


Fig. 37

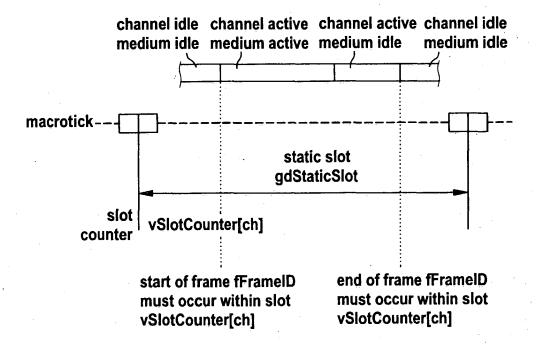


Fig. 38

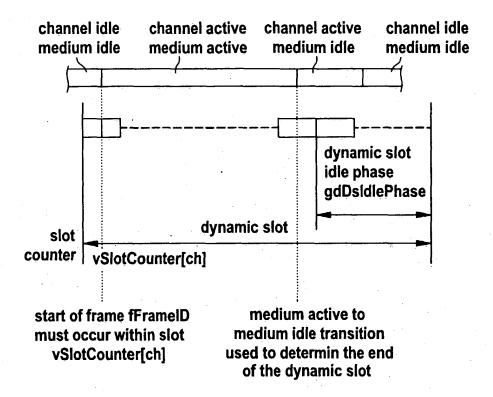
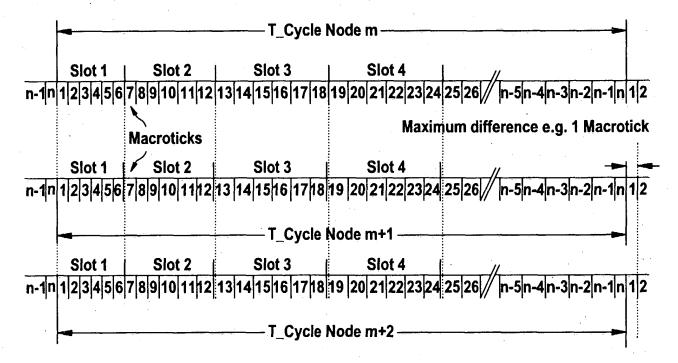
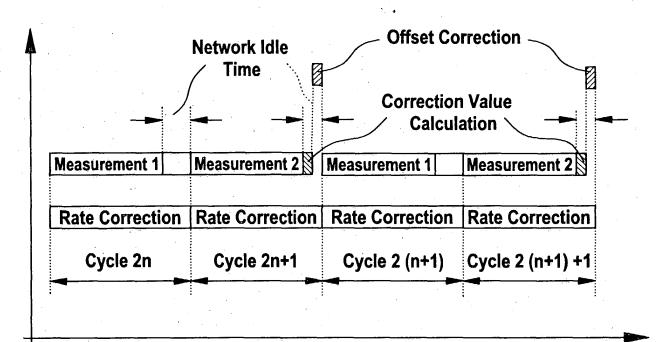


Fig. 39



n = number of Macroticks per cycle

Fig. 40



time

Fig. 41

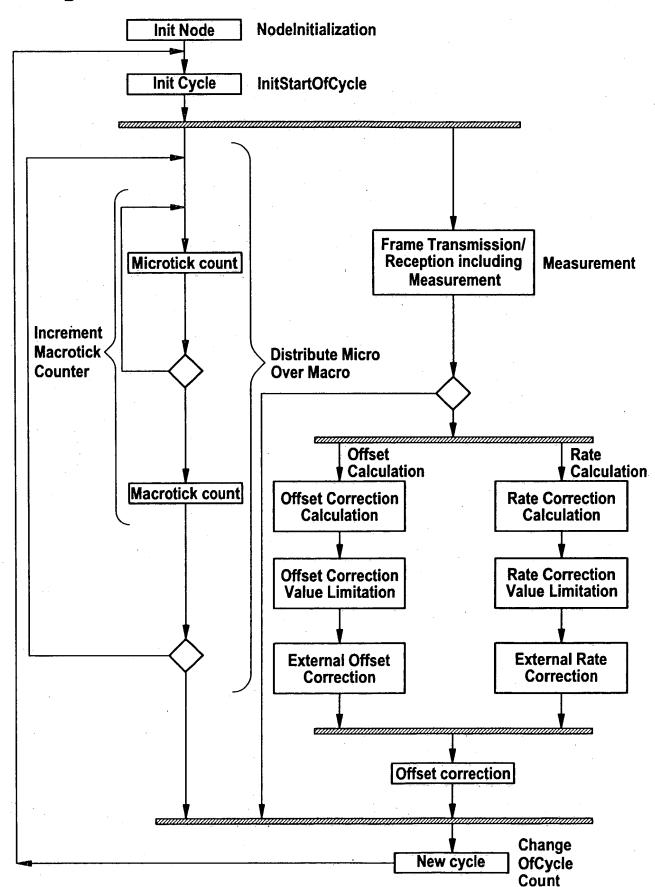


Fig. 42

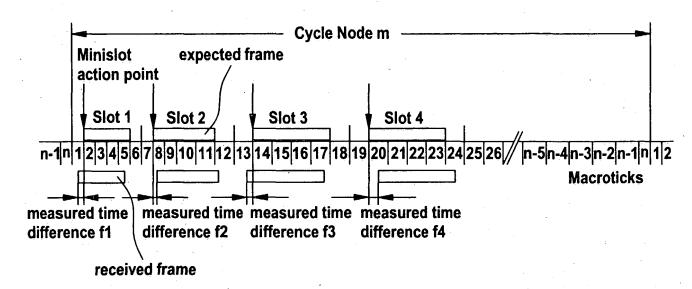
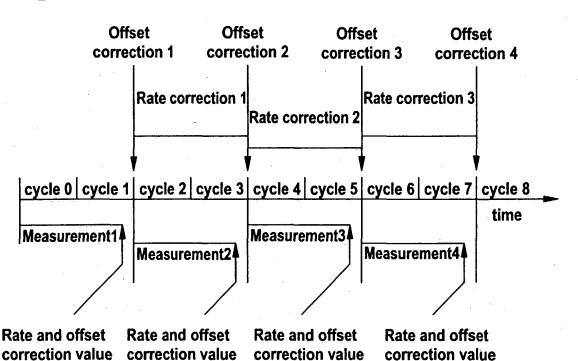


Fig. 44

calculation 1

calculation 2



calculation 3

calculation 4

Fig. 45

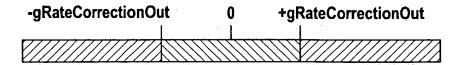
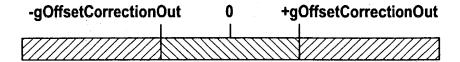


Fig. 46



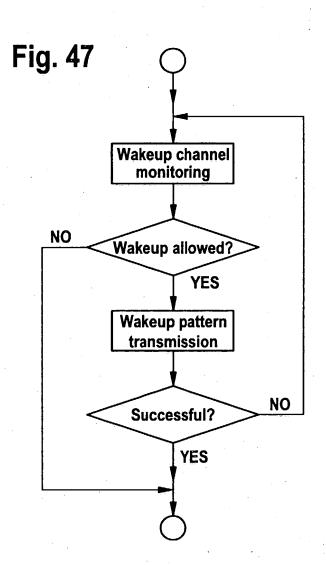


Fig. 48

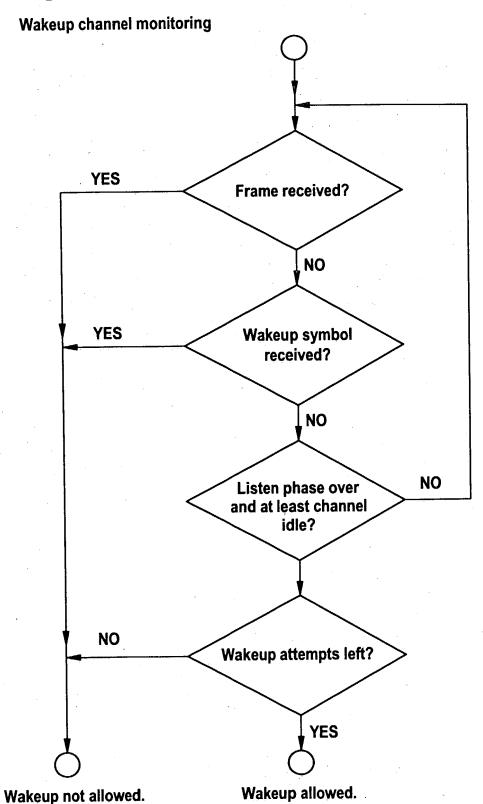


Fig. 49
Wakeup pattern transmission

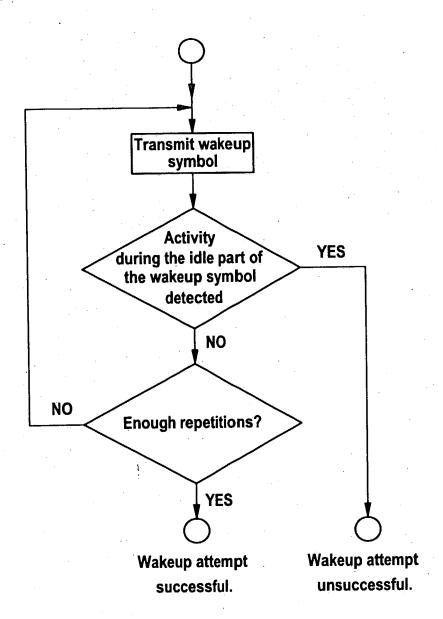
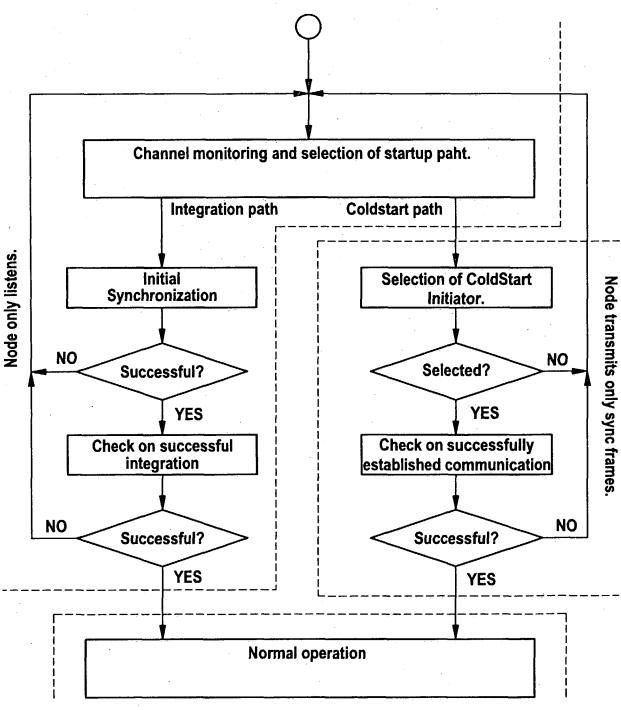


Fig. 50



Node communicates normally.

Fig. 51
Channel monitoring and selection of startup path.

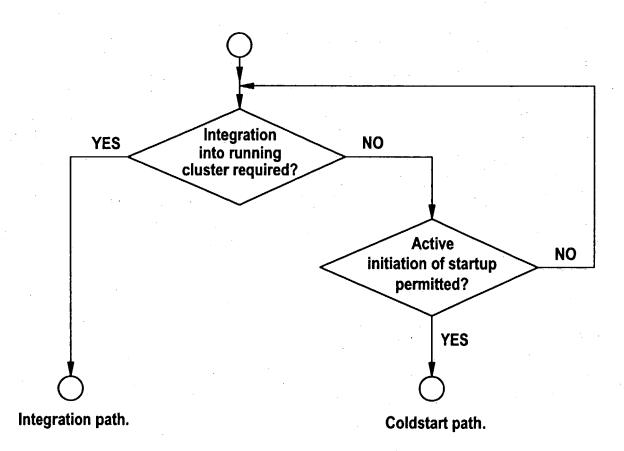


Fig. 52
Selection of ColdStart Initiator

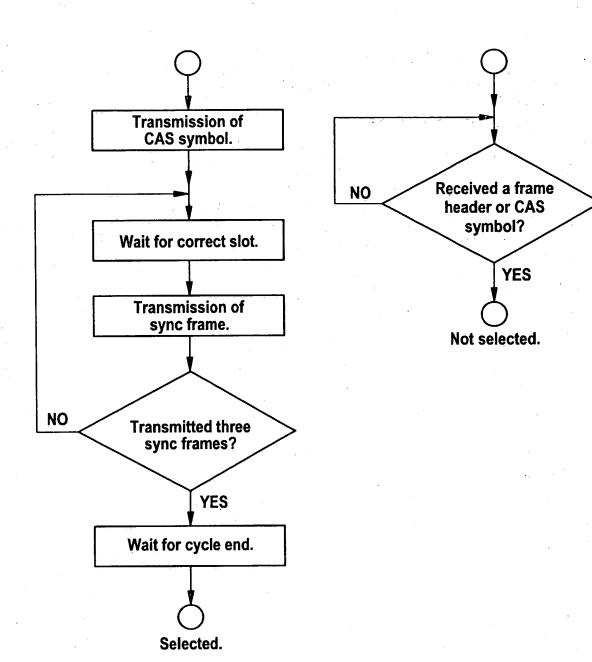


Fig. 53
Check on successfully established communication

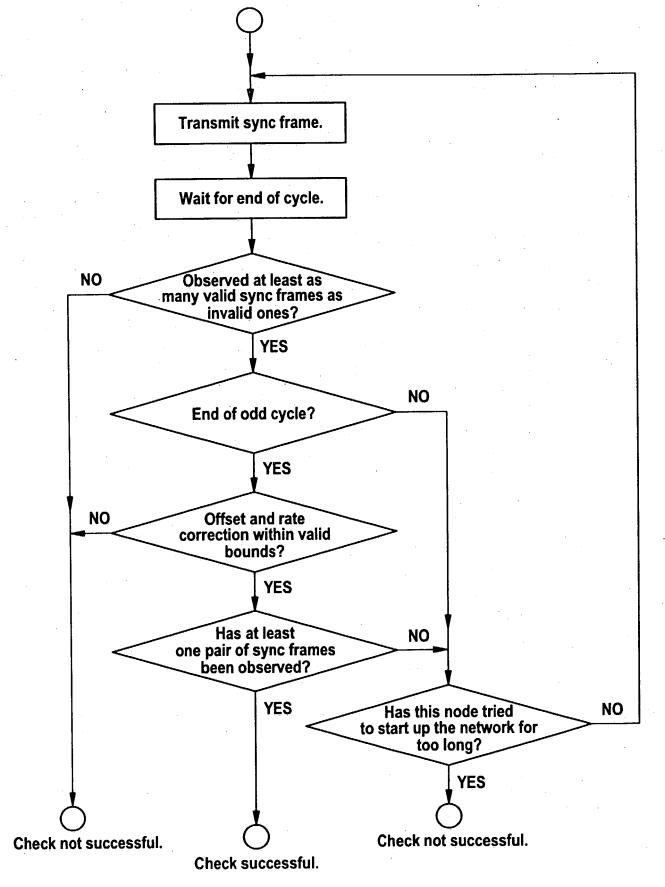
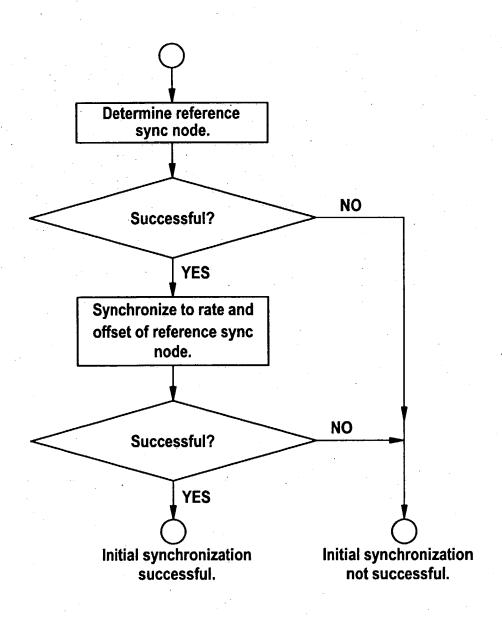
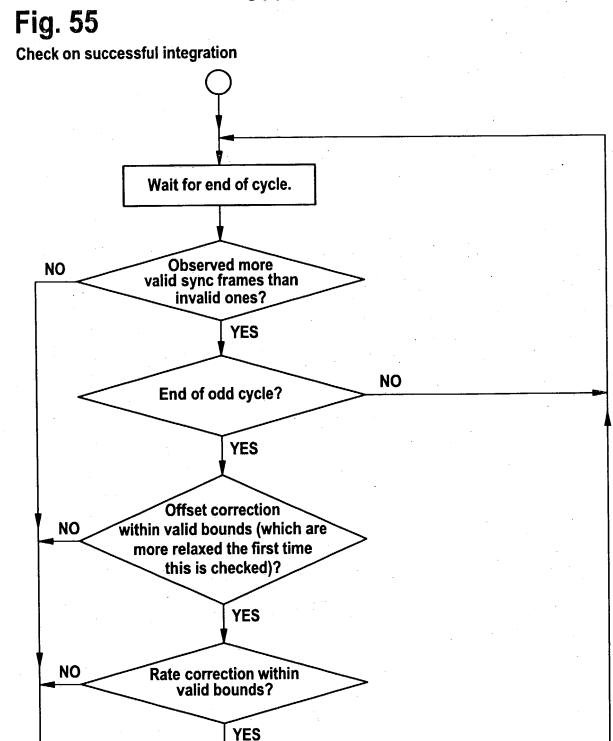


Fig. 54
Initial Synchronization





How many pairs of sync frames have been observed?

Check successful.

Check not successful.

>1

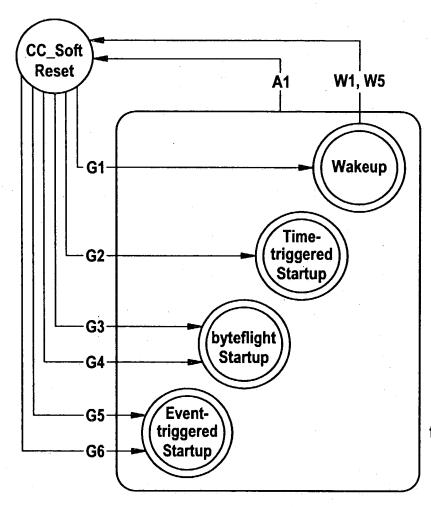
YES

NO

Is node a sync-node

allowed to transmit?

Fig. 56



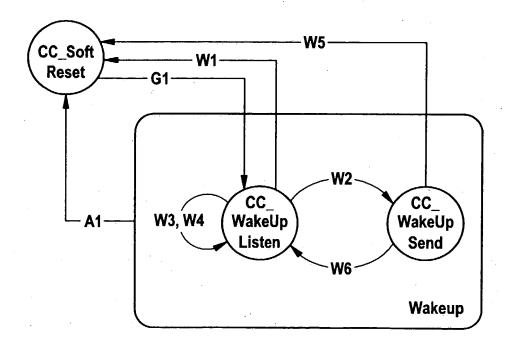
Wakeup state machine for the wakeup (WU) protocol mode

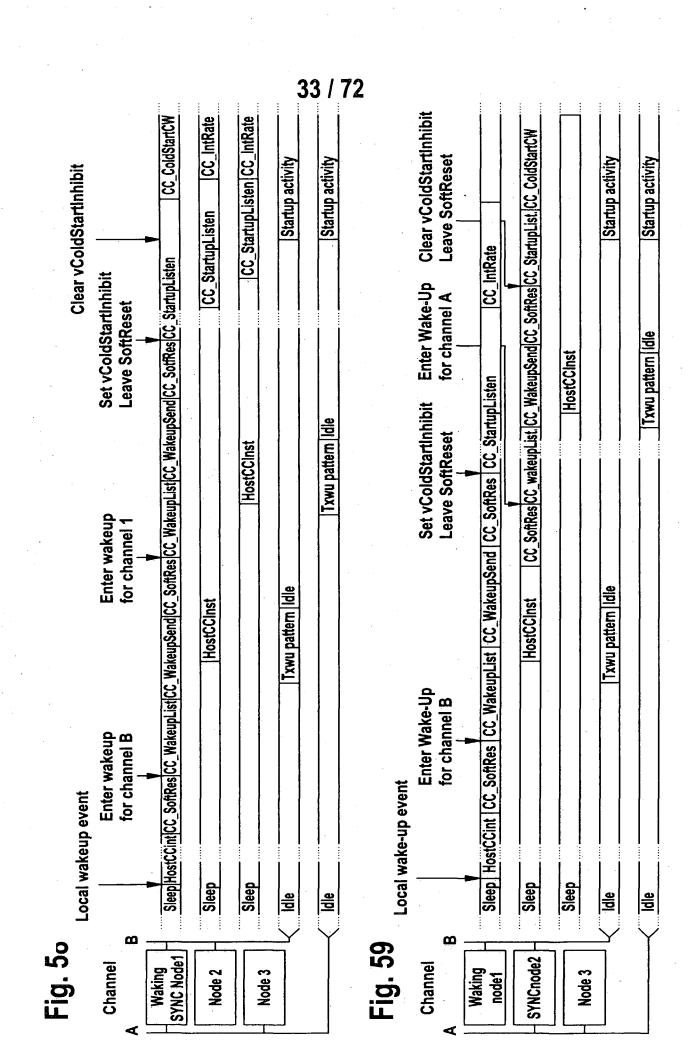
Startup state machine for the time-triggered (TT-D, TT-M) protocol modes

Startup state machine for the byteflight (BF) protocol mode

Startup state machine for the event-triggered (ET) protocol mode

Fig. 57





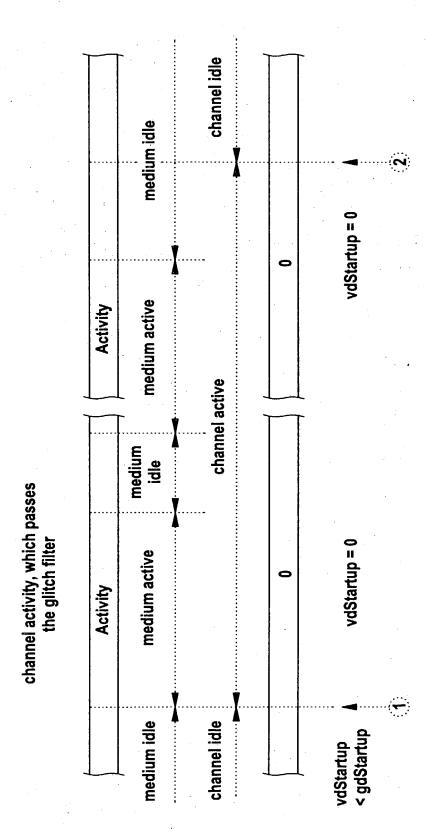


Fig. 60

Fig. 61

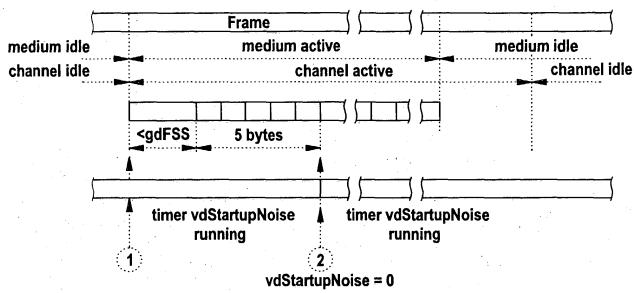


Fig. 62

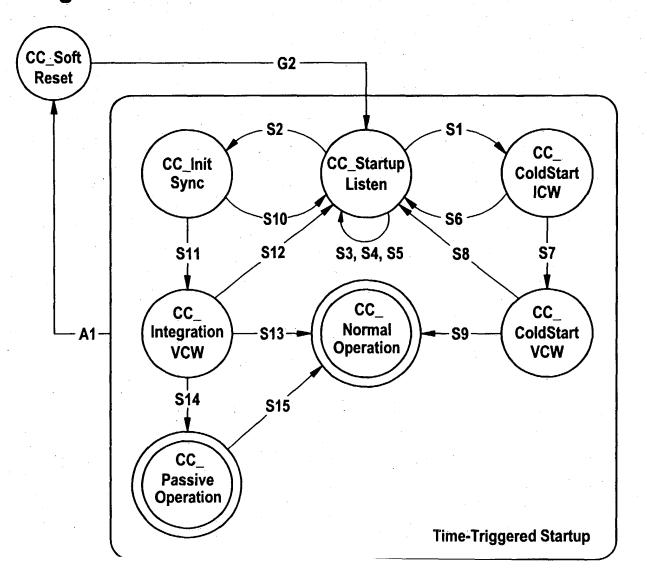


Fig. 63

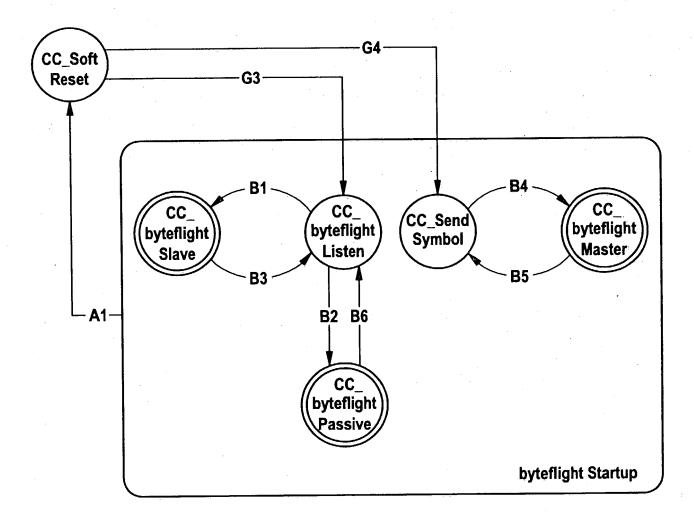


Fig. 64

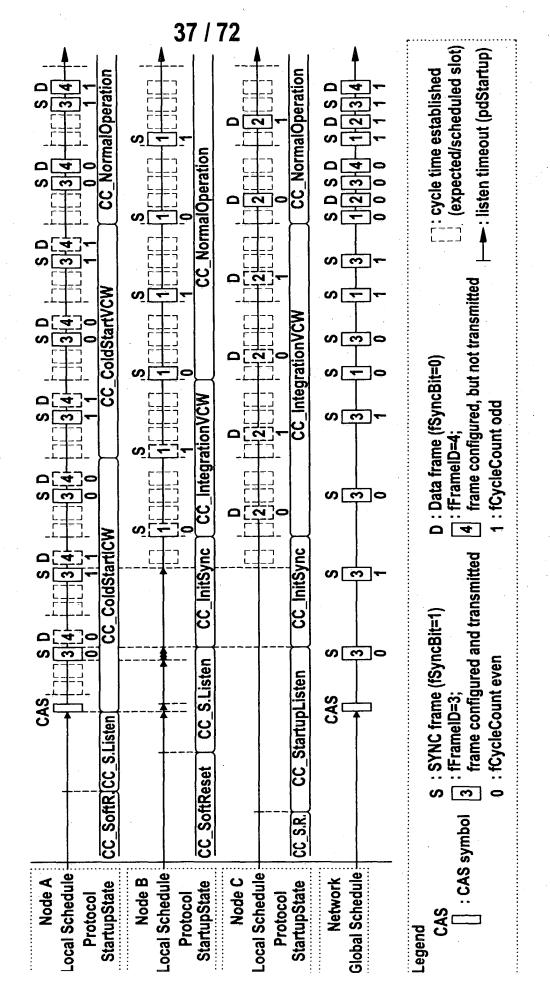


Fig. 6

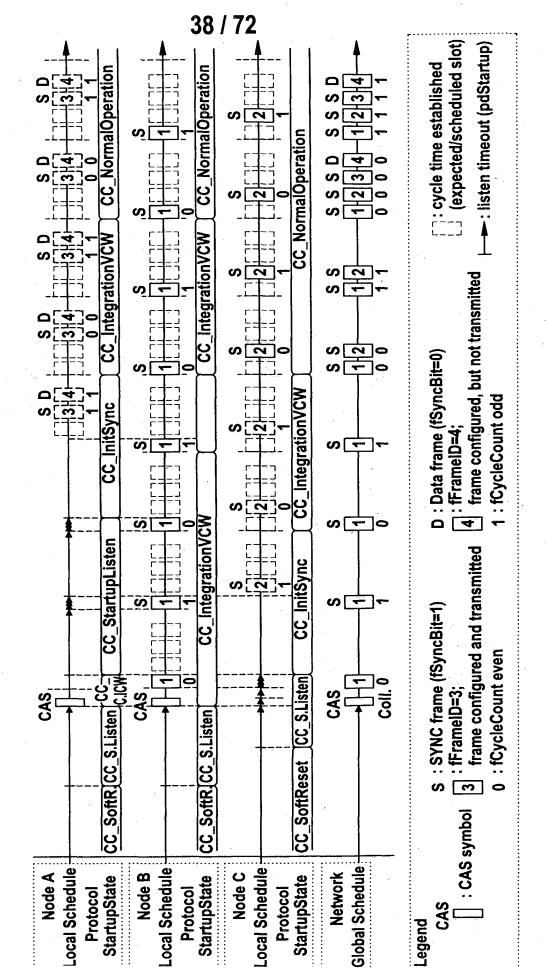


Fig. 66

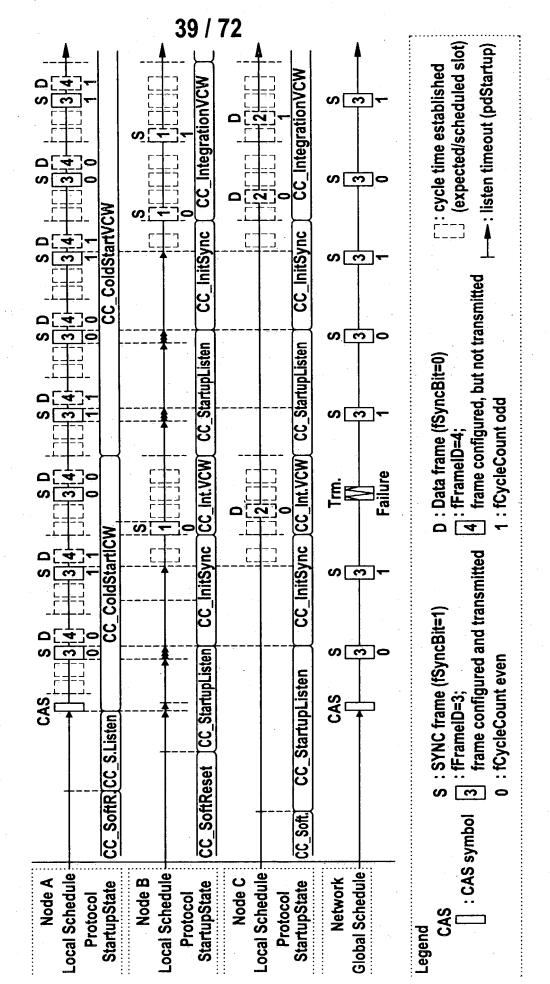


Fig. 67

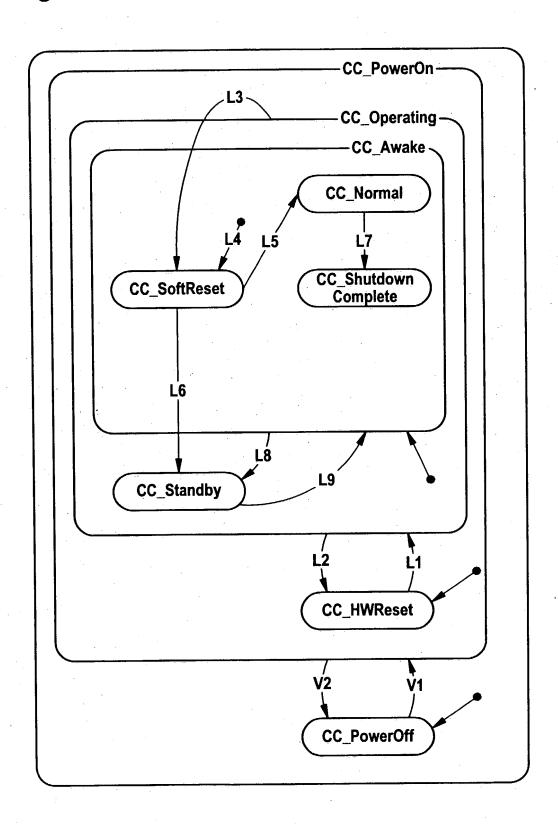


Fig. 68

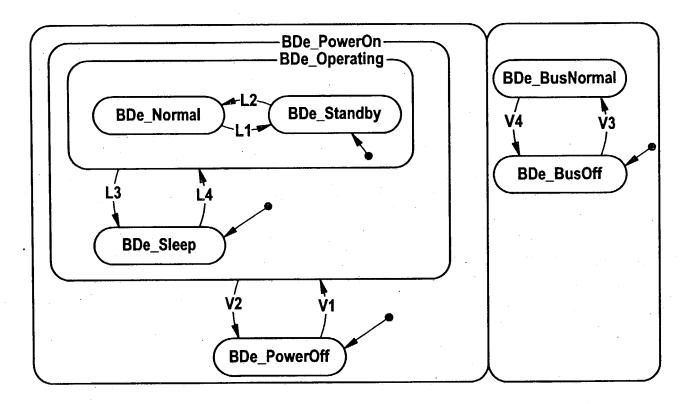


Fig. 69

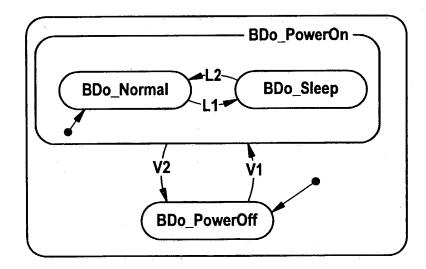


Fig. 70

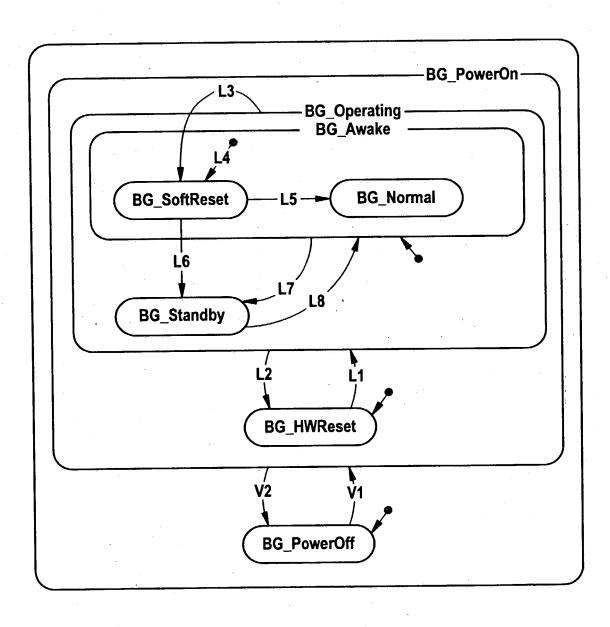


Fig. 71

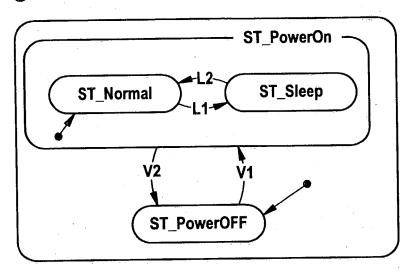


Fig. 72

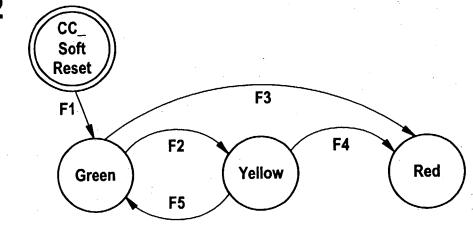
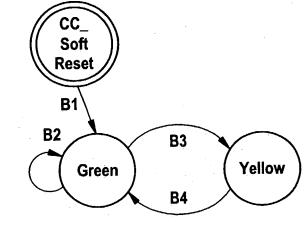


Fig. 73



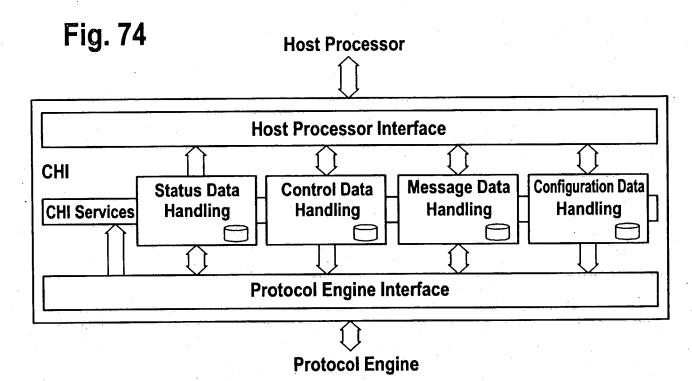


Fig. 75

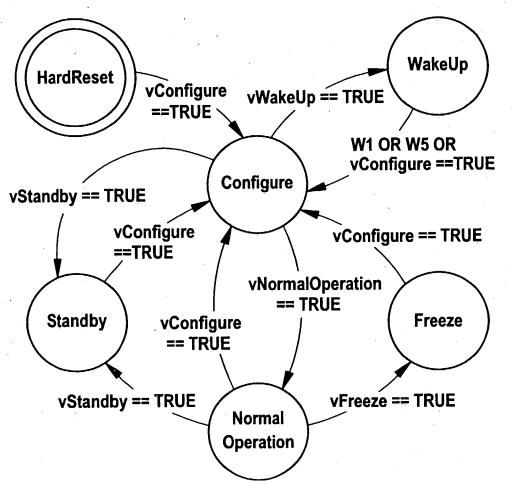


Fig. 76

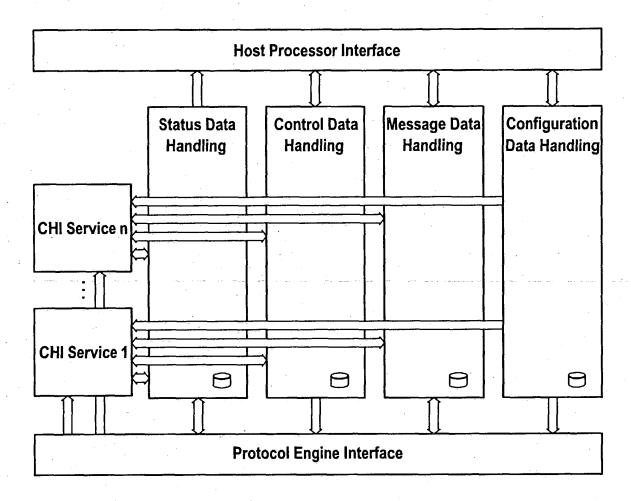


Fig. 77

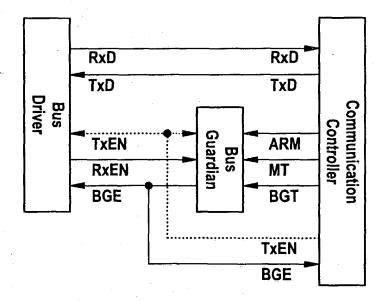


Fig. 78

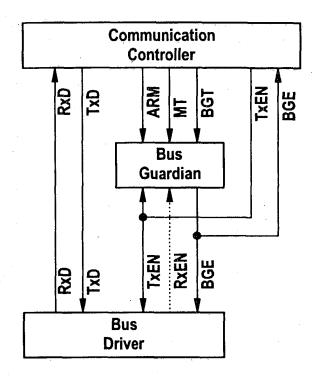


Fig. 79

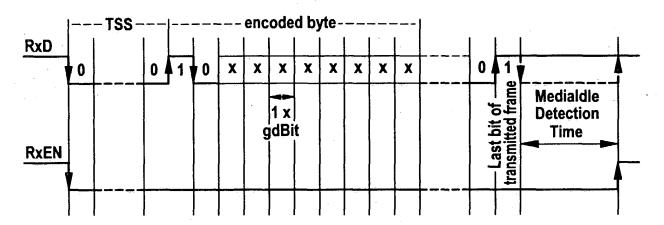


Fig. 80

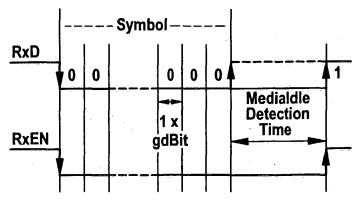


Fig. 81

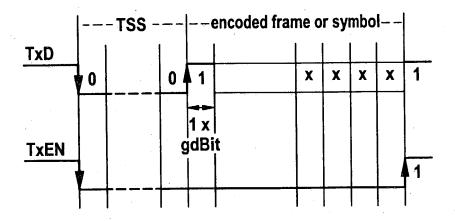
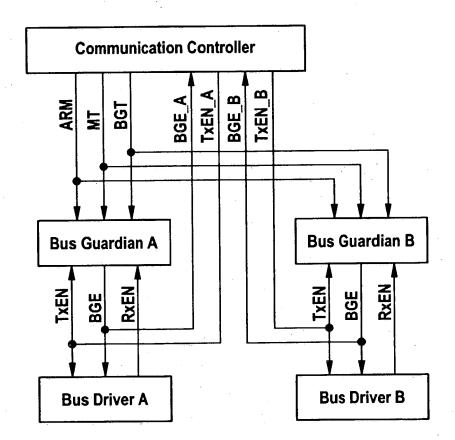
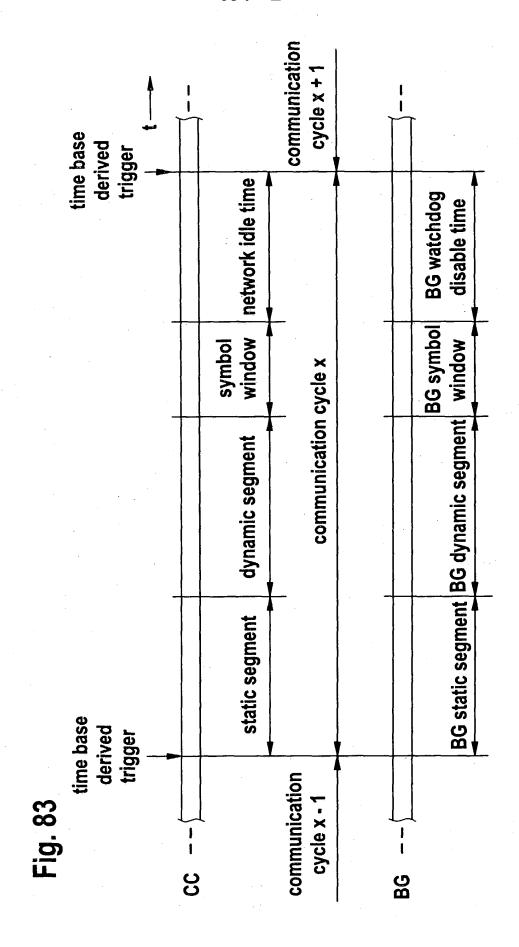
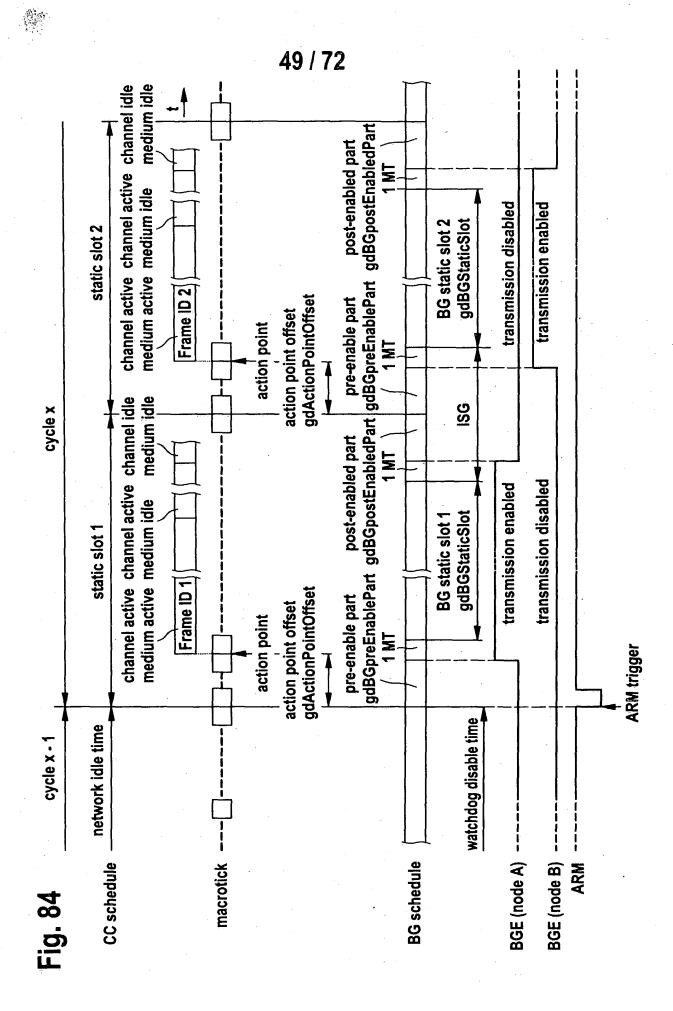
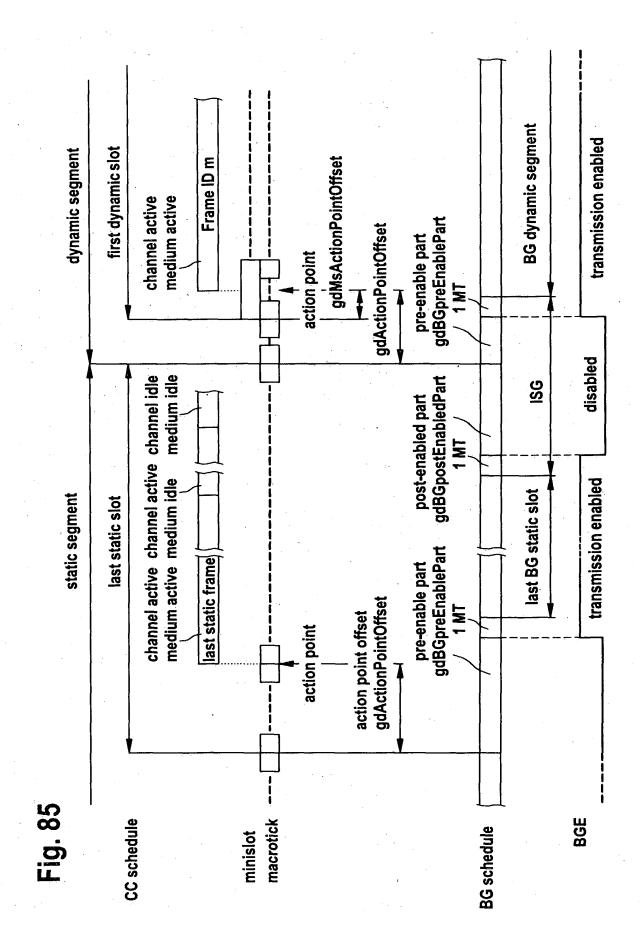


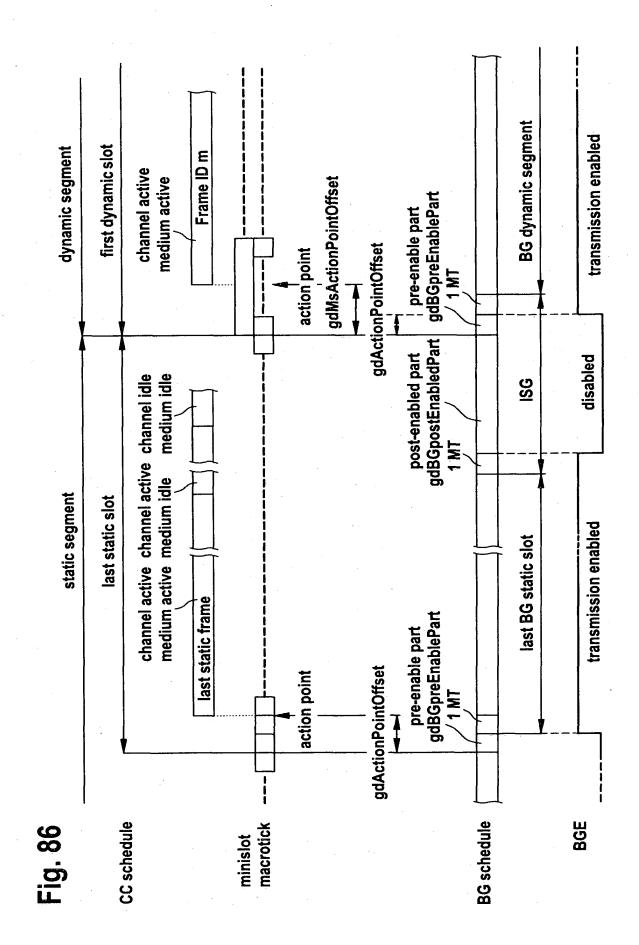
Fig. 82











BG cycle start

Fig. 87

minislot

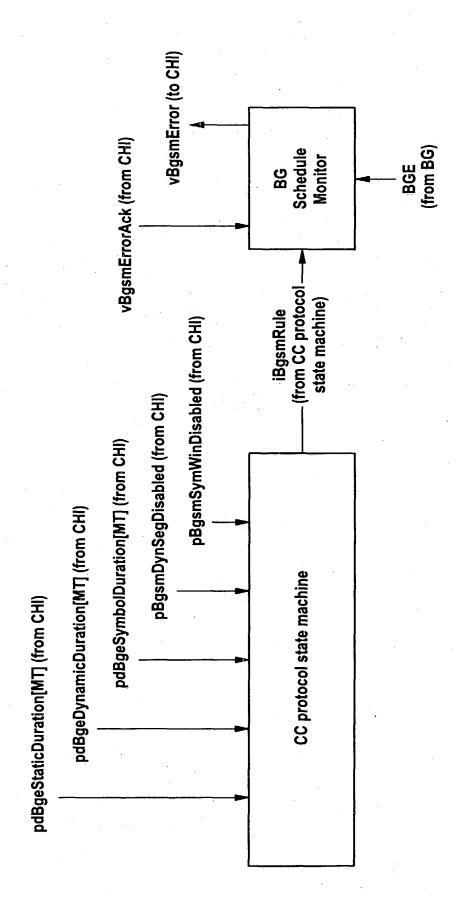
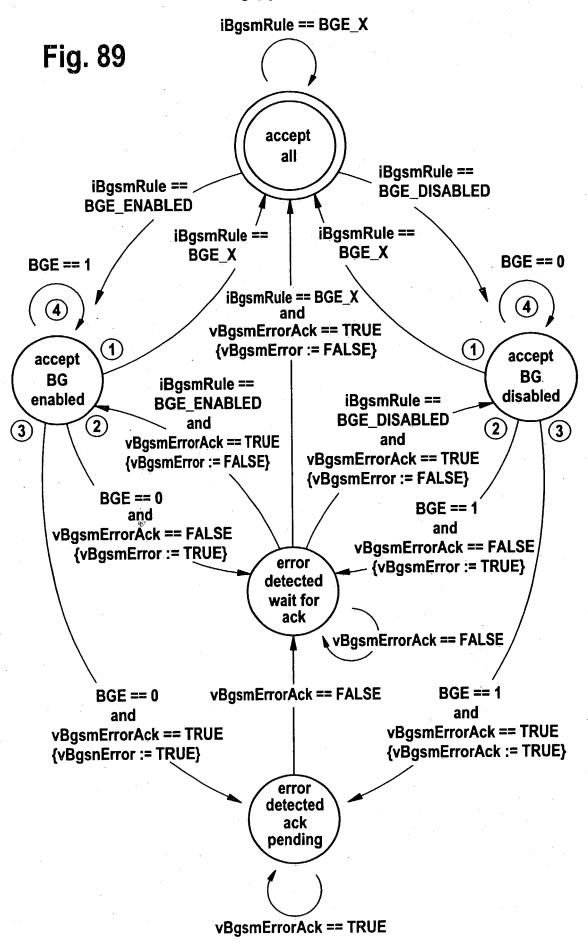


Fig. 88



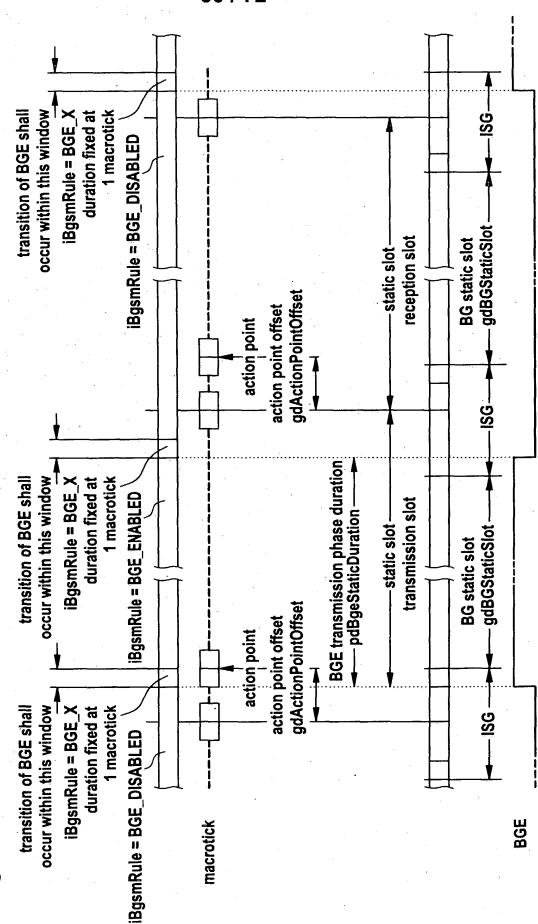


Fig. 90

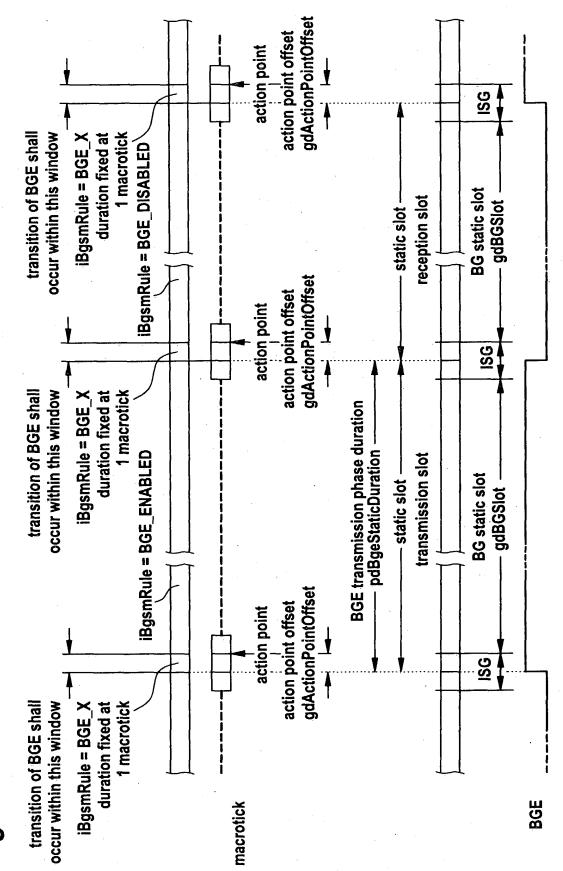
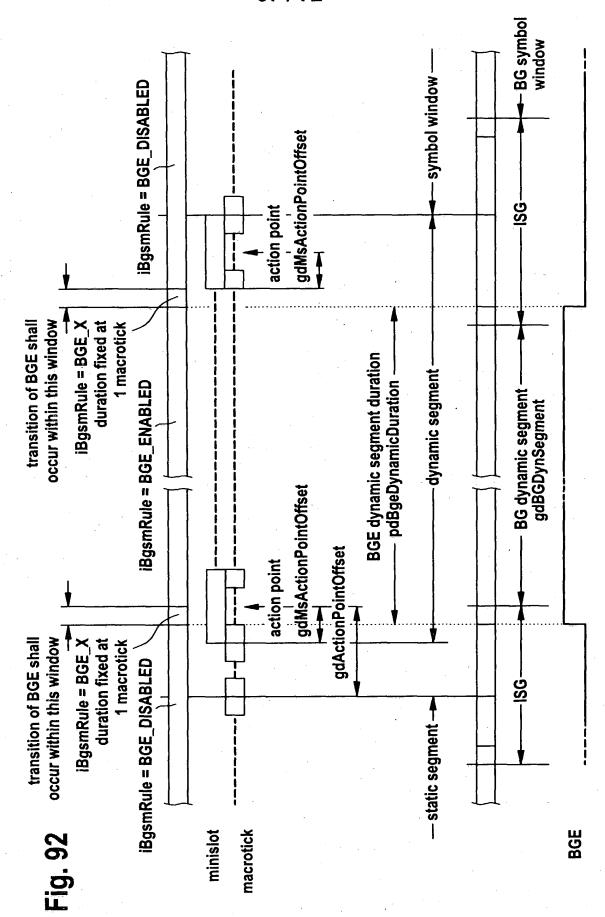


Fig. 91



macrotick minislot

BGE

transition of BGE shall

transition of BGE shall

Fig. 94

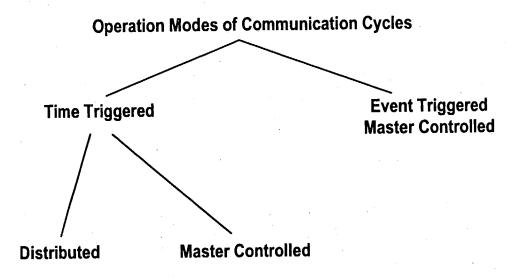
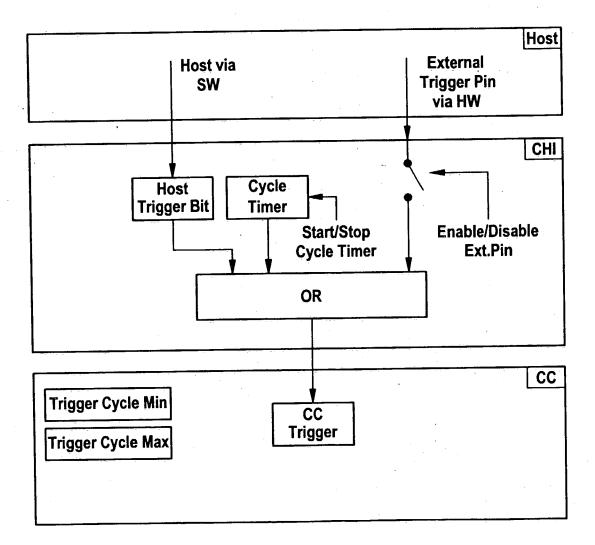
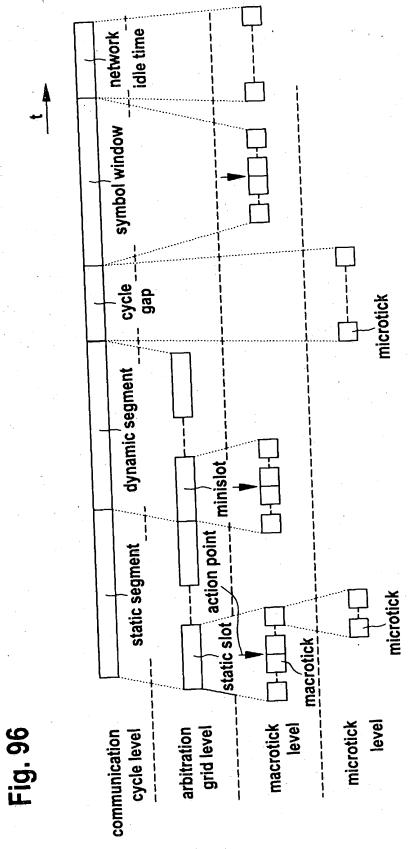
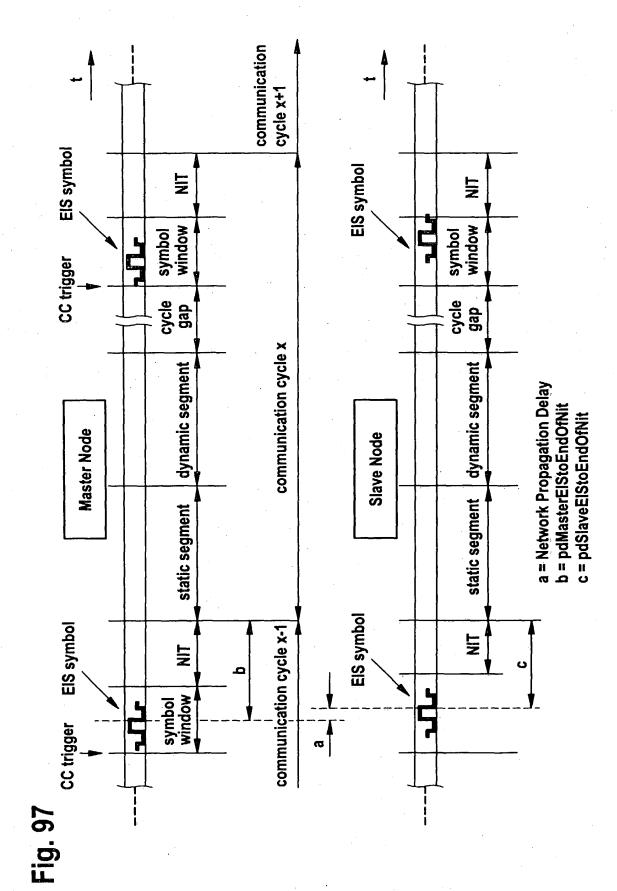


Fig. 95







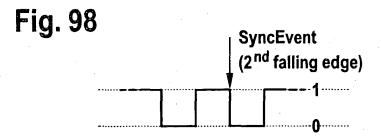
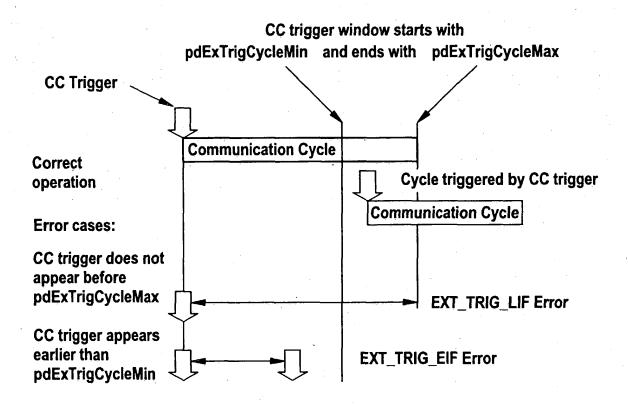
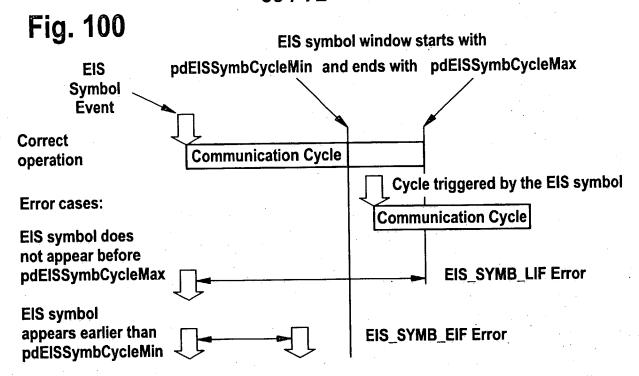


Fig. 99







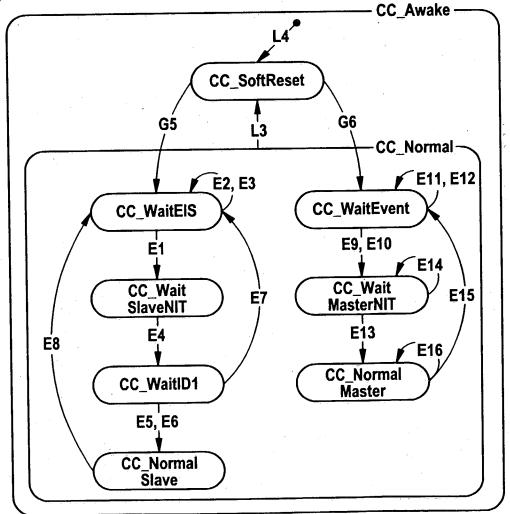
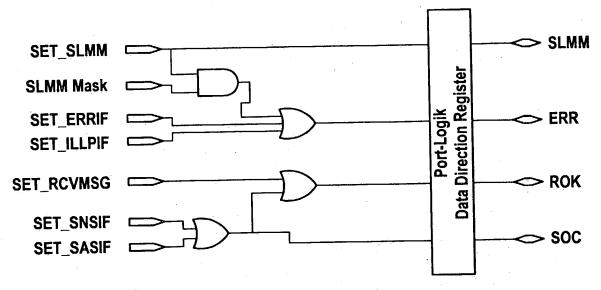


Fig. 102



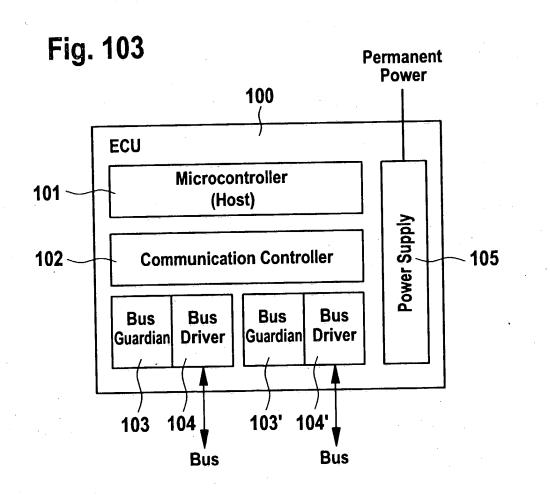


Fig. 104

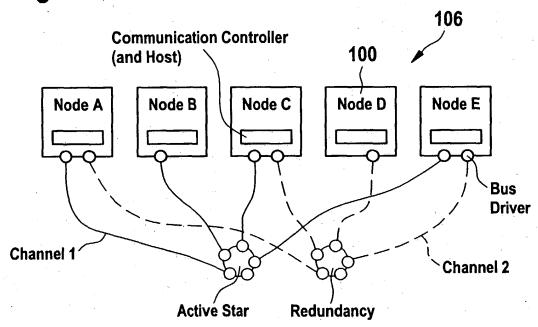


Fig. 105

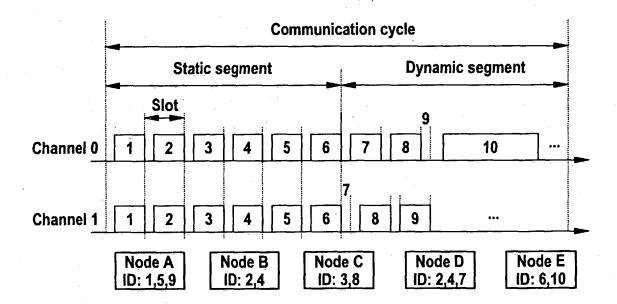


Fig. 106

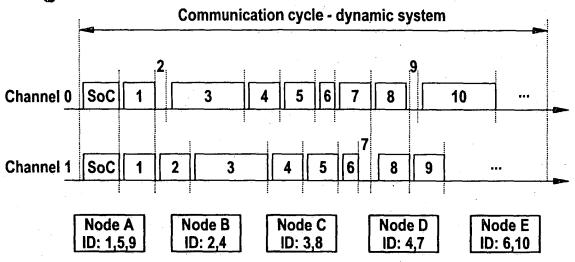
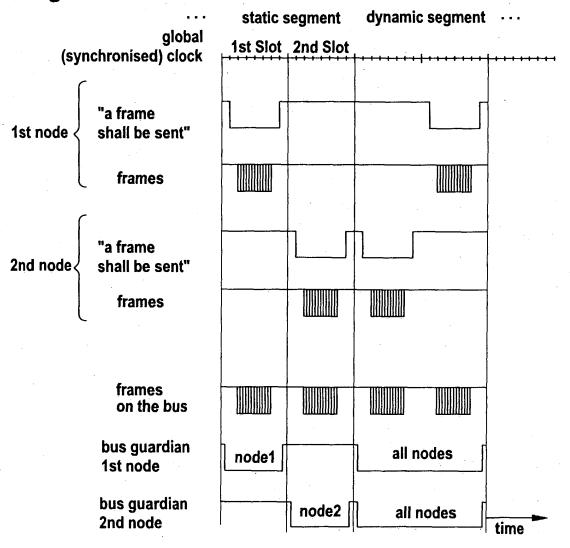


Fig. 107



		Heade	r 5 B	ytes	Data 0 246 Bytes		CRC Cod 3 Bytes		
Res	Frame ID	Sync	DLC	H-CRC	NF	CYCO	Message ID	Data	CRC
4	12	1	7	9	1	6	16	01968	24

Configurable

Fig. 109

Header 2 Bytes				Data 0 12 Bytes		CRC Code (15 Bit) + Frame Completion Bit			
			1		~				
ID I	RES	LEN		DATA		CRC	FCB		
8	4	4		0 96	,	15	1		

Fig. 110

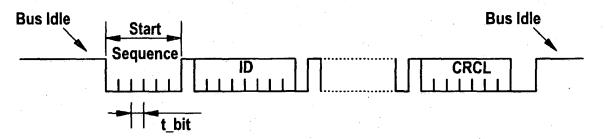


Fig. 111

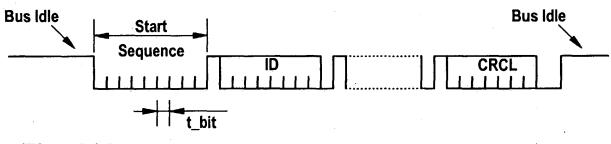


Fig. 112

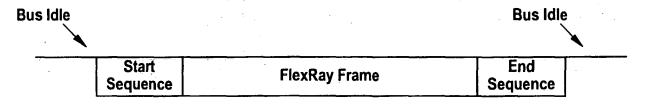


Fig. 113

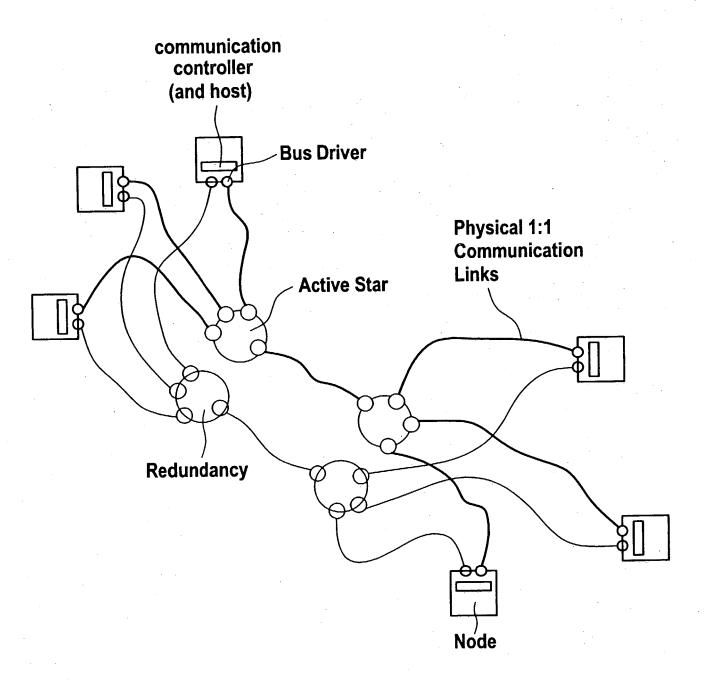


Fig. 114

